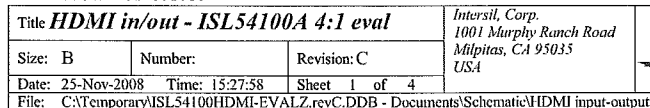
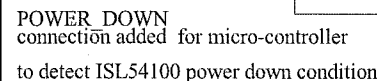


One resistor/inputPort added across its 5V/GND node to resolve uC detection of presense/loss of input +5V

**intersil**